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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/810,595	03/15/2001	Belford T. Coursey	MI22-1660	8140

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EXAMINER

HUYNH, YENNHU B

ART UNIT	PAPER NUMBER
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2813

DATE MAILED: 01/10/2002

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/810,595

Applicant(s)

COURSEY, BELFORD T.

Examiner

Yennhu B. Huynh

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 March 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 26-47 is/are pending in the application.
- 4a) Of the above claim(s) 38-47 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 26-37 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claims 1-25 have been cancelled by Amendment filed on 3/15/01.

Election/Restrictions

This application contains claims directed to the following patentably distinct species of the claimed invention:

- a). Species I: Claims 26-37, drawn to device of a memory circuitry
- b). Species II: Claims 38-47, drawn to device of a DRAM circuitry.

Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable. Currently, no claims generic.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

Should applicant traverse on the ground that the species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the species to be obvious variants or clearly admit on the record that this is the case. In either

instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103(a) of the other invention.

During a telephone conversation with Mark S. Matkin on 1/2/02 a provisional election was made without traverse to prosecute the invention of a). claims 26-37. Affirmation of this election must be made by applicant in replying to this Office action. Claims 38-47 are withdrawn from further consideration by the examiner, 37 CFR 1.142(b), as being drawn to a non-elected invention.

Specification

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The following title is suggested: Memory Circuitry.

Claim Rejections - 35 USC § 112

Claim 34 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 34, line 1 recites the limitation "the word lines". There is insufficient antecedent basis for this limitation in the claim.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 26-37 are rejected under 35 U.S.C. 103(a) as being unpatentable over
Background of The Invention in view of IBM Corp. (Cited by Applicant).

In the Background of The Invention as set forth at pages 1&2 of the specification, a memory circuitry is described including an array area with memory cells, and a peripheral are where peripheral circuitry which is configured to write to and read from the memory array is fabricated; the memory cells capacitor is in vertically elongated, in a cup or container shape.

However, Background of The Invention does not disclose a well comprises a base received over the word lines and memory cell storage capacitors received within the well with the topmost surfaces of capacitor storage node electrodes being fabricated elevationally proximate the outermost surface of the insulate layer.

IBM Corp. at fig1-6 in related art pp. 245-247 disclose a plurality of word lines over a semiconductor substrate; an insulating layer over the word lines and the substrate which having at least one well formed therein, the well comprise a base over the wordlines to define of a memory array area and peripheral area/transistor to the well; plurality of memory cell storage capacitors are in the well over the word lines, and the capacitor storage node electrodes having topmost surfaces received elevationally proximate the outermost of the insulate layer (fig. 1).

With respect to claims 28-30,32,34,35 & 37 the time, concentration, distance and thickness are considered to involve routine optimization while has been held to

be within the level of ordinary skill in the art, As noted In re Aller, the selection of reaction parameters such as temperature and concentration would have been obvious.

"Normally, it is to expected that a change in temperature, or in range, concentration, cycles, thickness, would be an unpatentable modification. Under some circumstance, however, changes such as these may be impart patentability to a process if the particular ranges claimed produce a new and unexpected result which is different in kind and not merely degree from the results of the prior art ... such ranges are termed "critical ranges and the applicant has the burden of proving such criticality ... More particularly, where the general conditions of a claim are disclosed in the prior art, it is not inventive to discover the optimum or workable ranges by routine experimentation." In re Aller 105 USPQ233, 255 (CCPA). See also In re Waite 77 USPQ 586 (CCPA 1948); In re Scherl 70 USPQ 204 (CCPA 1946); In re Irmischer 66 USPQ 314 (CCPA 1945); In re Norman 66 USPQ 308 USPQ 308 (CCPA 1945); In re Swenson 56 USPQ 372 (CPA 1942); In re Sola 25 USPQ 433 (CCPA 1935); In re Dreyfus 24 USPQ 52 (CCPA 1934).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to include a well comprises a base received over the word lines and memory cell storage capacitors received within the well, with the topmost surfaces of capacitor storage node electrodes being fabricated elevationally proximate the outermost surface of the insulate layer, for obtain a significantly desired height, by IBM Corp.' s process, and with the skill in the art of using the varying distances across

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the wafer, into Background of The Invention. This modification would complete a memory circuitry as claimed invention .

Cited Prior Art

Dennison (US. 5,250,457) at figs. 1-15 in related art col. 1-14 discloses a buried bit line array of memory cells. The process includes an array of word lines; an area where word lines to define an array of memory cells with grooves for formation of buried bit lines array of memory cells; insulating layer over the substrate and around the grooves; bit line contact/ capacitor storage node electrode. Dennison also disclose a layer of conductively doped polysilicon over the word lines such that the surface of the conductively doped polysilicon is elevationally above the word lines a selected height difference (p. 13). This reference is deemed relevant to the current invention.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yennhu Huynh whose telephone number is (703)308-6110. The examiner can normally be reached on Monday-Friday from 8:00 AM to 4:30PM.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

YNBH,

1/2/02



Chandra Chaudhari
Primary Examiner